

**Advanced technology  
for  
Specific Needs (close to silicon devices)**

**Thru hole :**

- Aspect ratio  $\leq 14$
- Drilling diameter 150  $\mu$

**Laser vias :**

- Sequential multilayers (3 levels)
- Stacked Microvias
  - Laser Holes 75  $\mu$

**Materials (RoHS) :**

- High Tg epoxy (Tg 175°C) completely compatible RoHS (Filler and phenolic hardener)
- Materials for high frequencies (4 to 15 GHz)
  - Green materials (Halogen free)
  - Low deformation materials (X,Y,Z)

**Lines & spacing :**

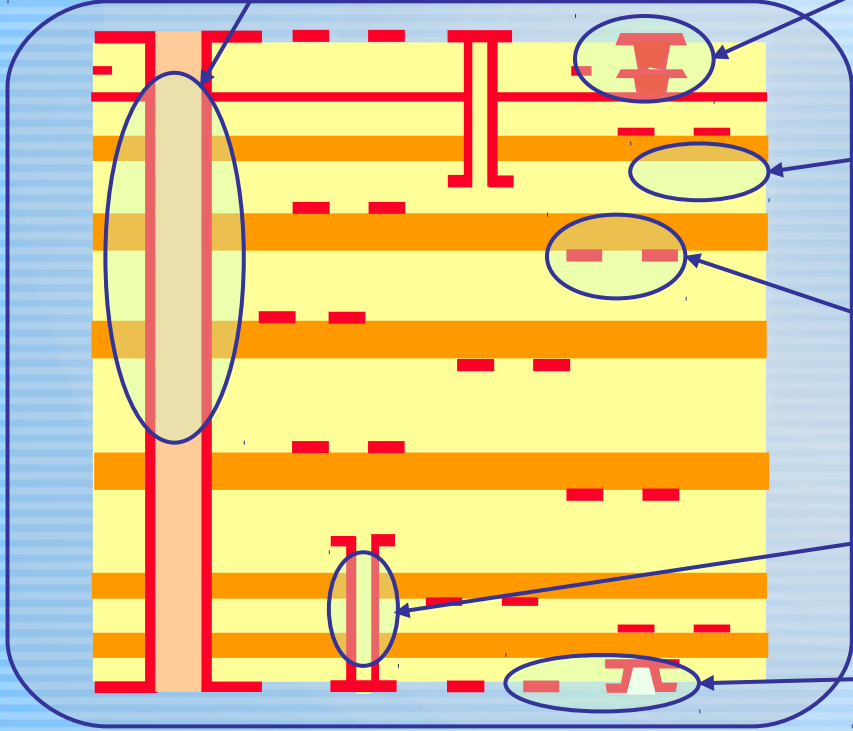
- AOI check
- Clean room class 100
- 40  $\mu$  lines / 60  $\mu$  spacing

**Blind vias :**

- drilling 100  $\mu$

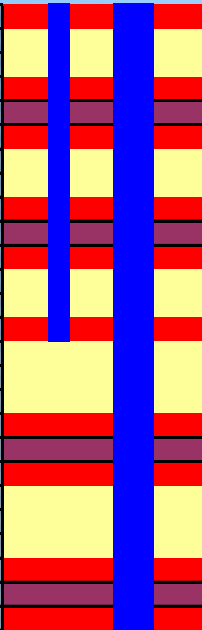
**Finishes :**

- Electroless nickel-gold
- Electroless tin - OSP
- HASL- Fused tin-lead
- Ionic contamination < 0.3  $\mu\text{g}/\text{cm}^2$

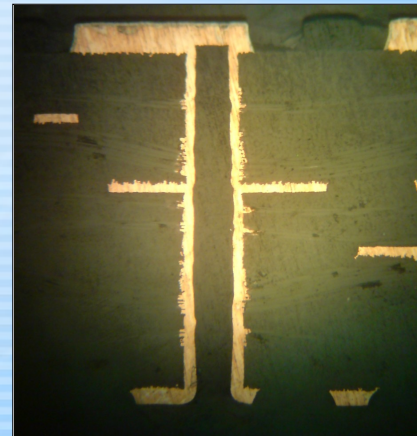
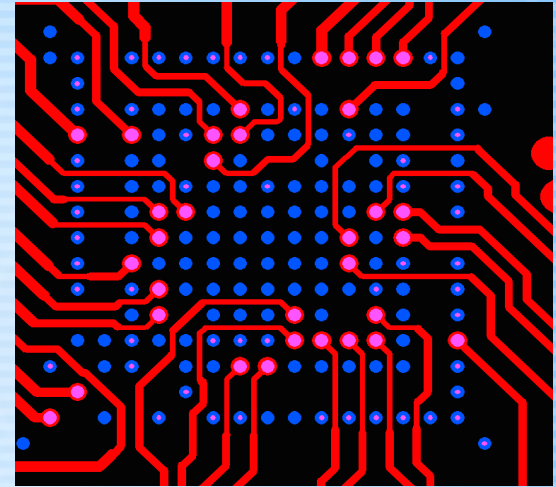


		Standard	Advanced	Emerging	Future
<b>1. Line and Space</b>	<i>Line Width Line Space</i>	100u 100u	75u 75u	50u 50u	20-35u 30u
<b>2. Drilled Via Size</b>	<i>Drill Size</i>	200u	150u	100u	75u
<b>3. MicroVia Size</b>	<i>Via Size</i>	100u	75u	60u	50u
<b>4. Drilled Aspect</b>	<i>Aspect Ratio</i>	12:1	18:1	24:1	30:1
<b>5. MicroVia Aspect</b>	<i>Aspect Ratio</i>	0.7:1	1:1	1.2:1	2:1
<b>6. Microvia Stack-up</b>	<i># Layers Buried Sub Stacked Vias</i>	1+1 No No	3+3 Yes Yes	5+5 Yes Yes	Any layer Yes Yes
<b>7. Hole Fill/Cap Plate</b>	<i>Min Hole Aspect Ratio</i>	200u 12:1	100u 18:1	75u 12:1	<75u >24

# High Density PCB's

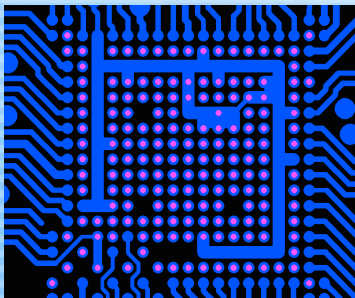
1	CU	9		50
	PPG	50		100
	PPG	50		
2	CU	18		18
	STRAT	100		100
3	CU	18		18
	PPG	50		86
	PPG	50		
4	CU	18		18
	STRAT	100		100
5	CU	18		18
	PPG	50		110
	PPG	50		
6	CU	9		27
	PPG	50		460
	PPG	50		
	PPG	50		
0	CU	0		
	STRAT	150		
0	CU	0		
	PPG	50		
	PPG	50		
	PPG	50		
7	CU	18	18	
	STRAT	508	500	
8	CU	18	50	

Pad layer 6: 250um drill 100um  
 Min line: 75 um (at base)  
 Min space: 75 um



## DESIGN

BGA 500um pitch (17\*17 = 289 balls)  
 External layer pad : 350 um drill 100um  
 External min line: 100 um

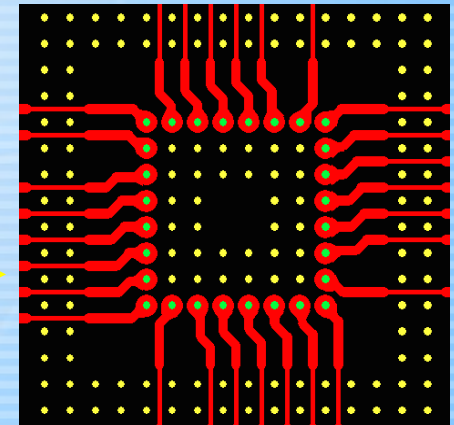
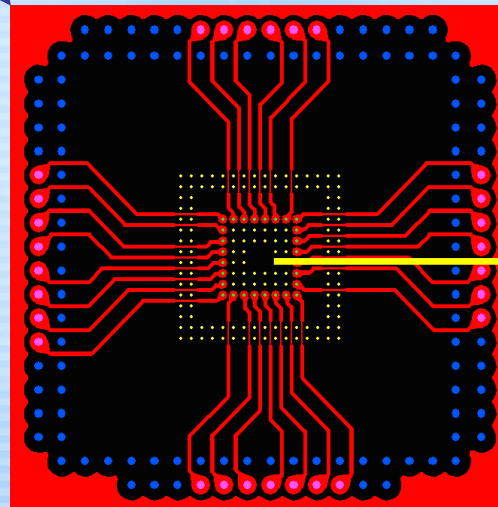
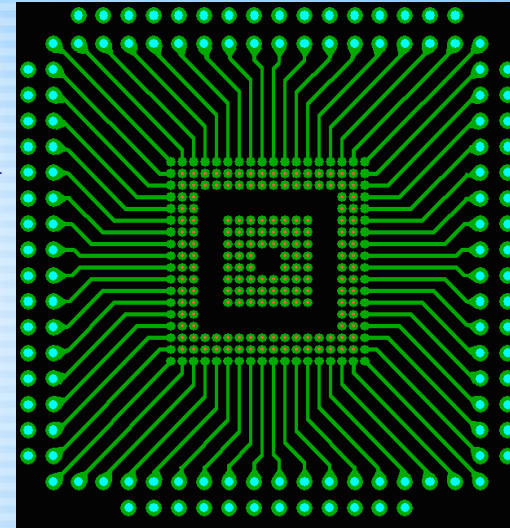


## BGA at 0.5 mm pitch : one line between 2 pads sequential board

COUCHE TYPE CUIVRE (\*)

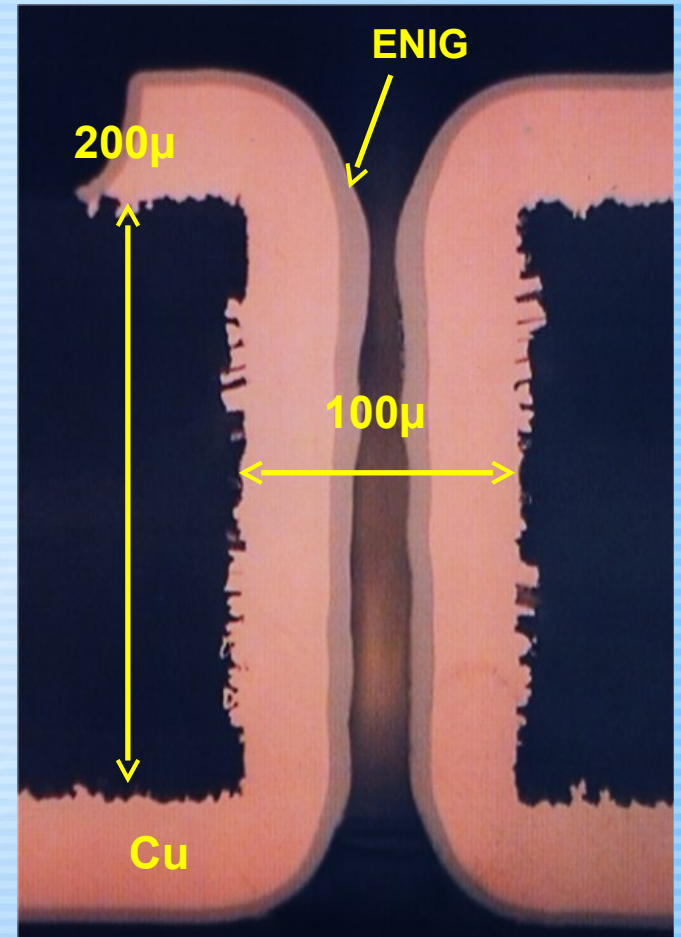
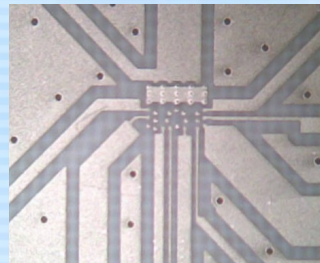
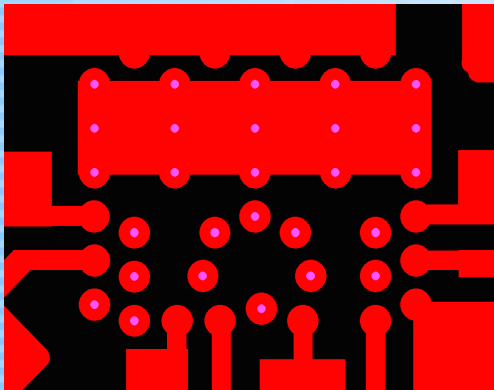
		(microns)	
1	E	45	====
2	P	18	-----
3	L	18	-----
4	P	18	-----
5	L	18	-----
6	EP	25	====
	L	18	-----
	L	18	-----
7	EP	25	====
8	L	18	-----
9	P	18	-----
10	L	18	-----
11	P	18	-----
12	E	45	====

==== cuivre "externe"  
----- cuivre "interne"



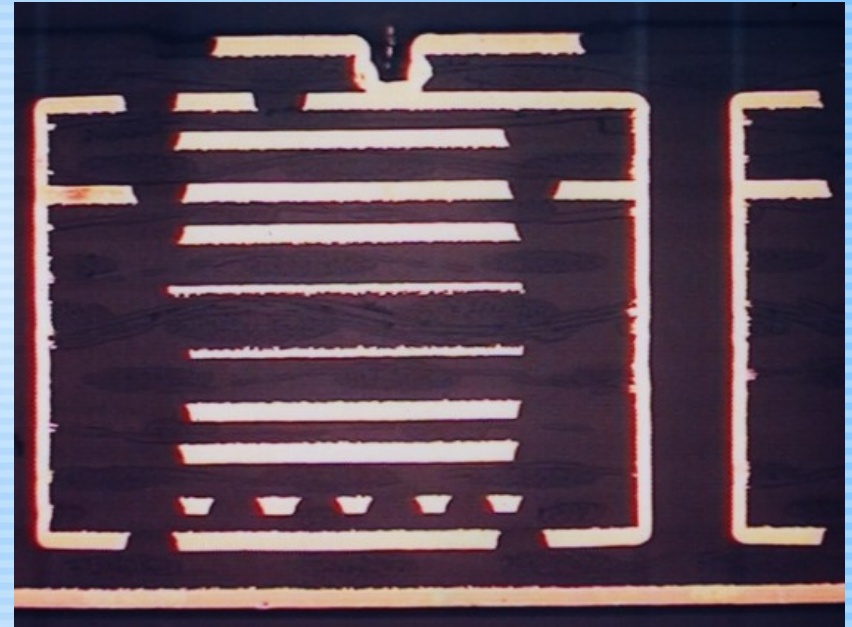
Drilled holes at 150µ  
60 µ lines

**Double sided board :**  
- Flip-chip at 250  $\mu$  pitch  
- 165  $\mu$  pad  
- Drilling at 100  $\mu$   
- Base material : Hitachi 679F



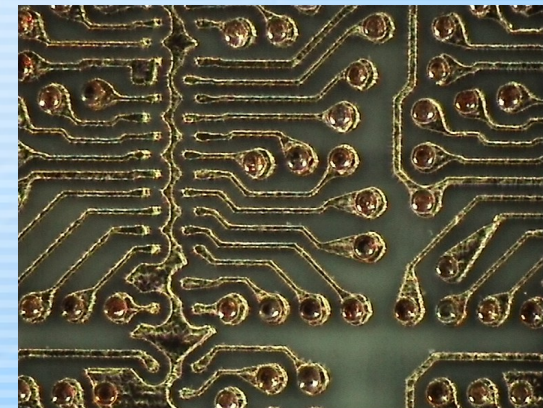
# High Density PCB's

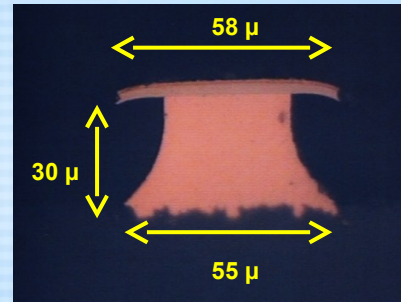
		(microns)		(microns)
1	E	45	==	32
			==	67
2	ELP	25	==	30
			==	38
3	ELP	25	==	30
			==	63
4	ELP	25	==	31
			==	43
5	ELP	25	==	30
			==	72
6	LP	12	---	12
			---	100
7	LP	12	---	13
			---	82
8	ELP	25	==	30
			==	43
9	ELP	25	==	32
			==	64
10	ELP	25	==	27
			==	36
11	ELP	25	==	31
			==	68
12	E	45	==	30



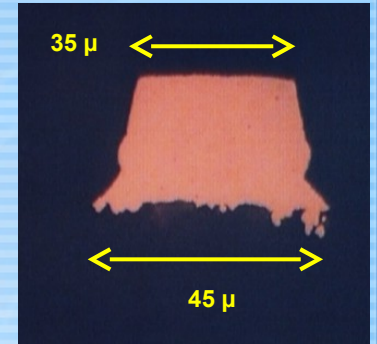
TOTAL		314	
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==== cuivre "externe"  
 ----- cuivre "interne"

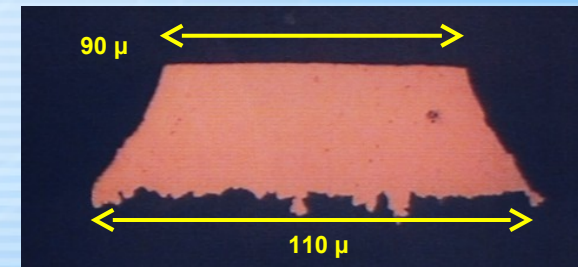
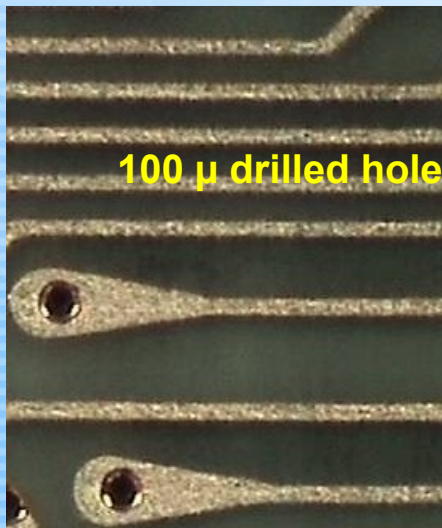




External line with Nickel & Gold



Inner line



Inner line



*Thank you!*